TOSHIBA Photocoupler GaAs Ired & Photo-Triac

TLP3502A

Trica Driver
Programmable Controllers
AC-Output Module
Solid State Relay

The TOSHIBA TLP3502A consists of a photo-triac optically coupled to a gallium arsenide infrared emitting diode in a 8 lead plastic DIP package.

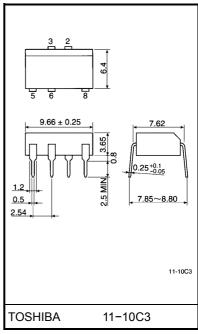
- Peak off-state voltage: 400V(min.)
- Trigger LED current: 10mA(max.)
- On-state current: 0.6A_{rms}(max.)
- Isolation voltage: 2500 V_{rms}(min.)
- UL recognized: UL1577, file no. E67349
- Trigger LED current

Classi- fication*	Trigger LED	Marking Of	
	$V_T = 6V$,	Classification	
	Min.	Max.	Glassification
(IFT5)	_	5.0	T5
(IFT7)	_	7.0	T5, T7
Standard	_	10	T5, T7, blank

*Ex. (IFT5); TLP3502A(IFT5)

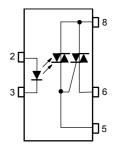
(Note) Application type name for certification test, please use standard product type name, i.e. TLP3502A (IFT5): TLP3502A

Unit in mm



Weight: 0.52g

Pin Configuration (top view)



- 2 : Anode
- 3 : Cathode
- 5 : Triac gate
- 6: Triac T1 8: Triac T2

Maximum Ratings (Ta = 25°C)

Characteristic		Symbol	Rating	Unit		
Forward current			Ι _Ε	50	mA	
LED	Forward current derating (Ta ≥	ΔI _F / °C	-0.7	mA / °C		
	Peak forward current (100µs pu	ılse, 100pps)	I _{FP}	1	Α	
	Reverse voltage	V _R	5	V		
	Junction temperature	Tj	125	°C		
	Off-state output terminal voltage	V_{DRM}	400	٧		
	On-state RMS current	Ta = 40°C	l±(DMO)	0.6	Α	
Detector		Ta = 60°C	IT(RMS)	0.45		
	On–state current derating (Ta ≥	ΔI _T / °C	-7.5	mA / °C		
	Peak current from snubber circl (100µs pulse, 120pps)	I _{SP}	2	А		
	Peak nonrepetitive surge current	I _{TSM}	5	Α		
	Junction temperature	Tj	120	°C		
Storage temperature range			T _{stg}	-40~125	°C	
Operating temperature range		T _{opr}	-20~80	°C		
Lead soldering temperature (10s)		T _{sol}	260	°C		
Isolatic	Isolation voltage (AC, 1min., R.H.≤ 60%) (Note)			2500	V _{rms}	

(Note) Device considered a two terminal: LED side pins shorted together and Detector side pins shorted together.

Recommended Operating Conditions

Characteristic	Symbol	Min.	Тур.	Max.	Unit
Supply voltage	V_{AC}	_	_	120	V_{ac}
Forward current	I _F	15	20	25	mA
Peak current from snubber circuit	I _{SP}	_	_	1	Α
Operating temperature	T _{opr}	-20	_	80	°C

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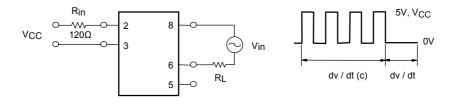
Individual Electrical Characteristics (Ta = 25°C)

Characteristic		Symbol	Test Condition		Min.	Тур.	Max.	Unit
LED	Forward voltage	V _F	I _F = 10mA		1.0	1.15	1.3	V
	Reverse current	I _R	V _R = 5V		_	_	10	μΑ
	Capacitance	C _T	V = 0, f = 1MHz			30	_	pF
Detector	Peak off-state current	I _{DRM}	V _{DRM} = 400V, Ta = 110°C			_	100	μΑ
	Peak on-state voltage	V_{TM}	I _{TM} = 0.75A			_	3.0	V
	Holding current	lΗ	_			_	25	mA
	Critical rate of rise of off–state voltage	dv / dt	V _{in} = 120V _{rms}	(Fig.1)	200	500	_	V / µs
	Critical rate of rise of commutating voltage	dv / dt (C)	$V_{in} = 120V_{rms}, I_T = 0.5A_{rms}$	(Fig.1)	_	5	_	V / µs

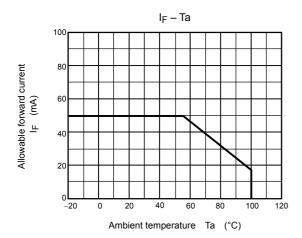
Coupled Electrical Characteristics (Ta = 25°C)

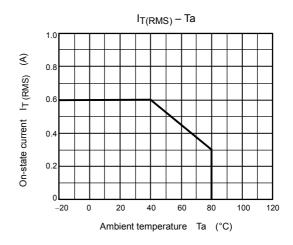
Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Trigger LED current	I _{FT}	V _T = 6V	_	_	10	mA
Capacitance (input to output)	C _S	V _S = 0, f = 1MHz	_	1.5	-	pF
Isolation resistance	R _S	V _S = 500V	5×10 ¹⁰	10 ¹⁴	_	Ω
	BVS	AC, 1 minute	2500	_	_	V _{rms}
Isolation voltage		AC, 1 second, in oil	_	5000	_	
		DC, 1 minute, in oil	_	5000	_	V _{dc}

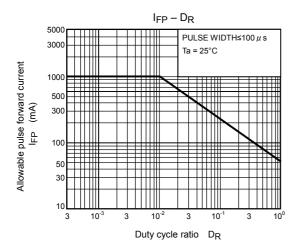
Fig.1: dv / dt test circuit

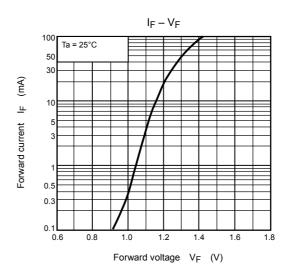


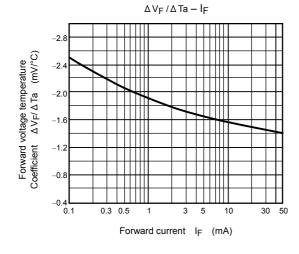
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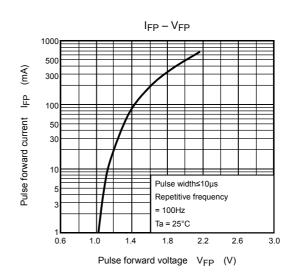


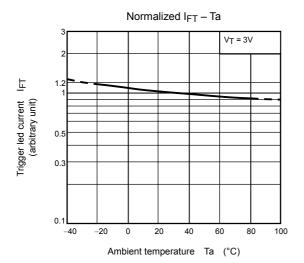


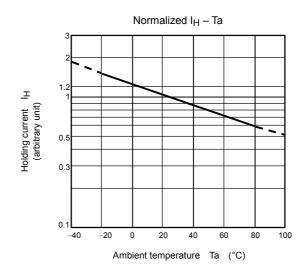


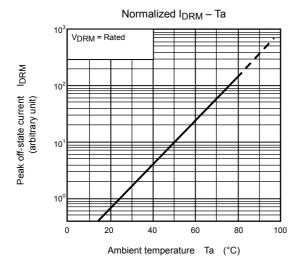


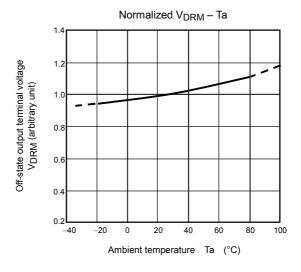


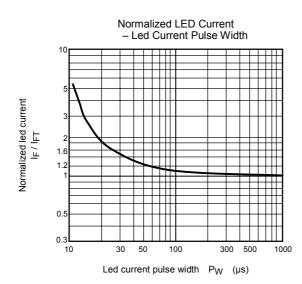












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